

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,111,385 B2
APPLICATION NO. : 10/675126
DATED : September 26, 2006
INVENTOR(S) : Chau et al.

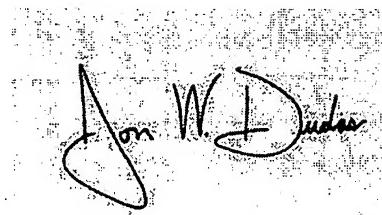
Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Col. 9, Line 27: "layer silicon" should read -- layer and the silicon --
Col. 9, Line 31: "layer thickness" should read -- layer with a thickness --
Col. 10, Line 14: "lay thickness" should read -- layer with a thickness --

Signed and Sealed this

Thirtieth Day of January, 2007



JON W. DUDAS
Director of the United States Patent and Trademark Office